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**From : Winston Hsu, Registration No. 41,526**

**Serial No.: 10/605,195**

**Patent No.: 7,073,008**

**Attorney Docket No.: MTKP0074USA**

**Subject: Citation of Prior Art under 37 CFR §1.501.**

**Total Pages: 39 pages (including cover page)**

**Winston Hsu 10/20/2006**

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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Patent Number	7,073,008	
	Issue Date	07/04/2006	
	First Named Inventor	Yuan, Ting Wu	
	Art Unit	2111	
	Examiner Name	DANG, KHANH	
Total Number of Pages in This Submission	38	Attorney Docket Number	MTKP0074USA

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Reply to Missing Parts/Incomplete Application <input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation <input type="checkbox"/> Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ <input type="checkbox"/> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): 1. Citation of Prior Art
Remarks _____		

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	North America Intellectual Property Corporation		
Signature	<i>Winston Hsu</i>		
Printed name	Winston Hsu		
Date	OCT. 20 2006	Reg. No.	41,526

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I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:			
Signature	<i>Zoe Huang</i>		
Typed or printed name	Zoe Huang	Date	OCT. 20 2006

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yuan-Ting Wu, Shu-Fang Tsai

5 Patentee: MediaTek Inc.

Patent No.: 7,073,008

Issue Date: 07/04/2006

Appl. No.: 10/605,195

Filing Date: 09/15/2003

Examiner: DANG, KHANH

Art Unit: 2111

Docket No.: MTKP0074USA

Confirmation No.: 2194

10

Title: METHOD OF FUNCTION ACTIVATION ON A BRIDGE SYSTEM

To: Commissioner for Patents

P.O. BOX 1450

15

Alexandria, VA 22313-1450

Subject: **Submission of Prior Art under 37 CFR §1.501**

Dear Sir,

20

The undersigned agent hereby submits the prior arts as cited in an Office communication mailed on July 14, 2006 for the counterpart China patent application number 200410078594.4. A form PTO/SB/42 detailing prior arts  
25 cited, copies of the patents or printed publications, and any necessary English translation are attached pursuant to 37 CFR 1.501 and MPEP 2205.

It's believed that the prior arts cited in the  
30 Office Action are pertinent and applicable to the patentability of the claims of the above-identified patent. It is the patentee's desire to have the

citation of prior art placed in the patent file.

The first cited document (CN 1420414A) provides a peripheral component interface, an interface circuit  
5 of SCSI. The interface circuit can be utilized as a backup interface circuit. The interface circuit can be automatically disabled when being initially coupled to a SCSI terminal, and can be enabled by an enabling signal when needed to act as the backup interface  
10 circuit. This function makes the peripheral component interface applicable to an error-tolerance digital transmission interface device. When an error of a master interface circuit occurs, the peripheral component interface works in replacement of the  
15 digital transmission interface function of the original master PCI SCSI interface circuit, such that the digital transmission will not be interrupted due to the error of the master interface circuit.

20 The second cited document (AI-YING WANG, "COMPUTER STRUCTURE AND ARCHITECTURE", Jul. 1995 2nd ed., Tsinghua University Press, PROC, ISBN 7-302-01788-3) illustrates SCSI interface signal lines as shown in Fig. 8.17 (Page 304) of the second cited document,  
25 where DB0, DB1, ..., DB7 represent digital bus digits 0-7, DBP represents a digital calibration digit, ACK is utilized for acknowledgement to REQ, REQ represents a request signal line, ATN represents an answer signal line for responding, RST represents a reset signal line,  
30 SEL represents a selection signal line, BSY represents a busy signal line, C/D represents a command/data signal line, I/O represents an I/O signal line, and

MSG represents a message signal line. A main device  
adapting interface is coupled to an external  
controller interface through the SCSI interface signal  
lines, as shown in Fig. 8.17. As further disclosed in  
5 the second cited document (Lines 1-10, Page 369), I/O  
interface basic functions comprise: 1. IMPLEMENTING  
COMMUNICATION CONTROL BETWEEN MAIN DEVICE AND  
PERIPHERAL DEVICES, where the communication control  
comprises sync control, device selection, interrupt  
10 control, etc., and DMA devices may have the function  
of accessing a storage unit and the function of sending  
addresses for accessing; 2. IMPLEMENTING DATA  
BUFFERING TO ACHIEVE SPEED MATCHING BETWEEN MAIN  
DEVICE AND PERIPHERAL DEVICES, where in an interface  
15 circuit, one or more data buffer registers are  
typically installed, and during transmission, data is  
first sent into the data buffer registers and then sent  
to target device or main device; and 3. RECEIVING MAIN  
DEVICE COMMANDS, PROVIDING DEVICE INTERFACE STATUSES,  
20 AND CONTROLLING DEVICES ACCORDING TO THE MAIN DEVICE  
COMMANDS.

The third cited document (CN 1333964A) is a  
counterpart China patent application of US Patent  
25 No. 6,253,268. Please refer to US Patent No. 6,253,268  
for more information.

It is respectfully requested that it be made of  
record in the application. The undersigned agent  
30 sincerely hopes that the examiner places the documents  
listed on the form into the patent file.



Respectfully submitted,

5 Winston Hsu

Date: OCT. 20 2006

Winston Hsu, Patent Agent No. 41,526

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15 time, i.e. 9 AM in D.C. = 9 PM in Taiwan.)